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February 23, 2006

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Om P. Agrawal; Jinghui Zhu
Assignee: Lattice Semiconductor Corporation
Title: Block Oriented Architecture For A Programmable Interconnect Circuit
Serial No.: 10/022,464 Filed: 12/14/2001
Examiner: Vibol Tan Group Art Unit: 2819
Docket No.: M-9642 US Confirmation No. 1715

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) This Transmittal Letter; (1 page)
- (2) Response to Office Action (13 pages)
- (3) Terminal Disclaimer (1 page)
- (4) Submission of Formal Drawing Transmittal and Formal Drawings (10 pages)



No additional fee is required.



The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After</u> <u>Amendment</u>		Highest No. Previously <u>Paid</u> <u>For</u>		Present <u>Extra</u>		<u>Rate</u>		Additional <u>Fee</u>
Total Claims	25	Minus	27	=	0	x	\$50.00	\$	0
Independent Claims	4	Minus	4	=	0	x	\$200.00	\$	0
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application							\$	
<u>Total additional fee for this Amendment:</u>								\$	<u>0</u>
<input checked="" type="checkbox"/>	Terminal Disclaimer Fee								\$130.00
<input checked="" type="checkbox"/>	Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.								
<input checked="" type="checkbox"/>	Please charge our Deposit Account No. 50-2257 in the amount of							\$	<u>130.00</u>
<input checked="" type="checkbox"/>	Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 50-2257								
Total:								\$	<u>130.00</u>

Certificate of Mailing

I hereby certify that this correspondence is sent via First Class
Mail to the Commissioner for Patents, Alexandria, VA 22313-
1450, on the date shown below.

Tina Kavanaugh

February 23, 2006

Respectfully submitted,

Jonathan W. Hallman
Attorney for Applicants
Reg. No. 42,622



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Om P. Agrawal; Jinghui Zhu
Assignee: Lattice Semiconductor Corporation
Title: Block Oriented Architecture For A Programmable Interconnect Circuit
Serial No.: 10/022,464 Issue Date December 14, 2001
Examiner: Vibol Tan Group Art Unit: 2819
Docket No.: M-9642 US Confirmation No. 1715

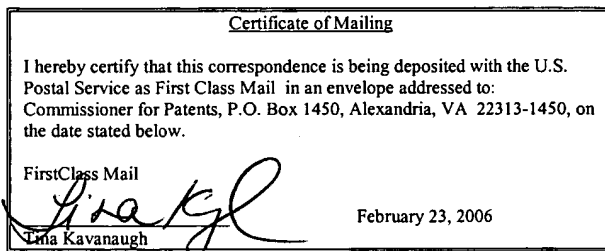
Irvine, California
February 23, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit nine (9) sheets of formal drawings, consisting of Figures 1-7, 8A-8B, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (949) 752-7040.



Respectfully submitted,


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